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Commissioner for Patents
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Alexandria, VA 22313-1450



Attorney for Applicant

Date: 05 June 2008



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JUN 10 2008
Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 7,373,645
Application No.: 10/689,312
Applicant : BEAUMONT, Mark
Filed : 20 October 2003
Title : Method For Using Extrema To Load Balance A Loop Of Parallel Processing Elements
TC/A.U. : 2195
Examiner : WAI, Eric Charles
Docket No. : PAT001067-000

5/13/08

SUBMISSION OF REFERENCES UNDER 37 CFR 1.501

Attached is a submission in the above identified patent listing references which are pertinent and applicable to the patent because they were cited in related domestic patent applications. The inclusion of the attached document in the patent file is requested.

Respectfully submitted,



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Dated: 5 June 2008

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TRANSMITTAL LETTER

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P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing are the following:

1. Submission of References Under 37 CFR 1.501; and
2. 37 CFR 1.501 Information Disclosure Citation in a Patent (PTO/SB/42) (1 p.).

The Commissioner is hereby authorized to charge any fees due in connection with this filing to our Deposit Account No. 50-3013. A copy of this Transmittal Letter is enclosed.

Respectfully submitted,



Edward L. Pencoske
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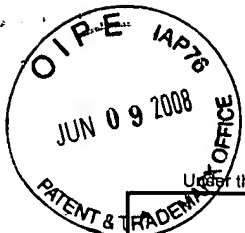
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Dated: 05 June 2008



PTO/SB/42 (01-08)

Approved for use through 06/30/2008. OMB 0651-0031

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**37 CFR 1.501
INFORMATION DISCLOSURE CITATION
IN A PATENT**

(Sheet 1 of 1)

Docket Number (Optional)

PAT001067-000

Patent Number

7,373,645

Applicant

BEAUMONT, Mark

Issue Date

13 May 2008

Art Unit

2195

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4,386,413	5-83	Vignes et al.			
	4,633,387	12-86	Hartung et al.			
	4,992,933	2-91	Taylor			
	5,109,512	4-92	Bahr et al.			
	5,701,482	12-97	Harrison et al.			
	6,392,822 B1	5-02	Takahashi			
	6,587,938 B1	7-03	Eilert et al.			
	6,651,082 B1	11-03	Kawase et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Rudolf, Larry; Slivkin-Allalouf, Miriam; Upfal, Eli; A Simple Load Balancing Scheme for Task Allocation in Parallel Machines; 1991 ACM
	Finnet, Thomas; Calculus; Second Edition; 1994
EXAMINER	DATE CONSIDERED

This collection of information is required by 37 CFR 1.501. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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